

Figure 1

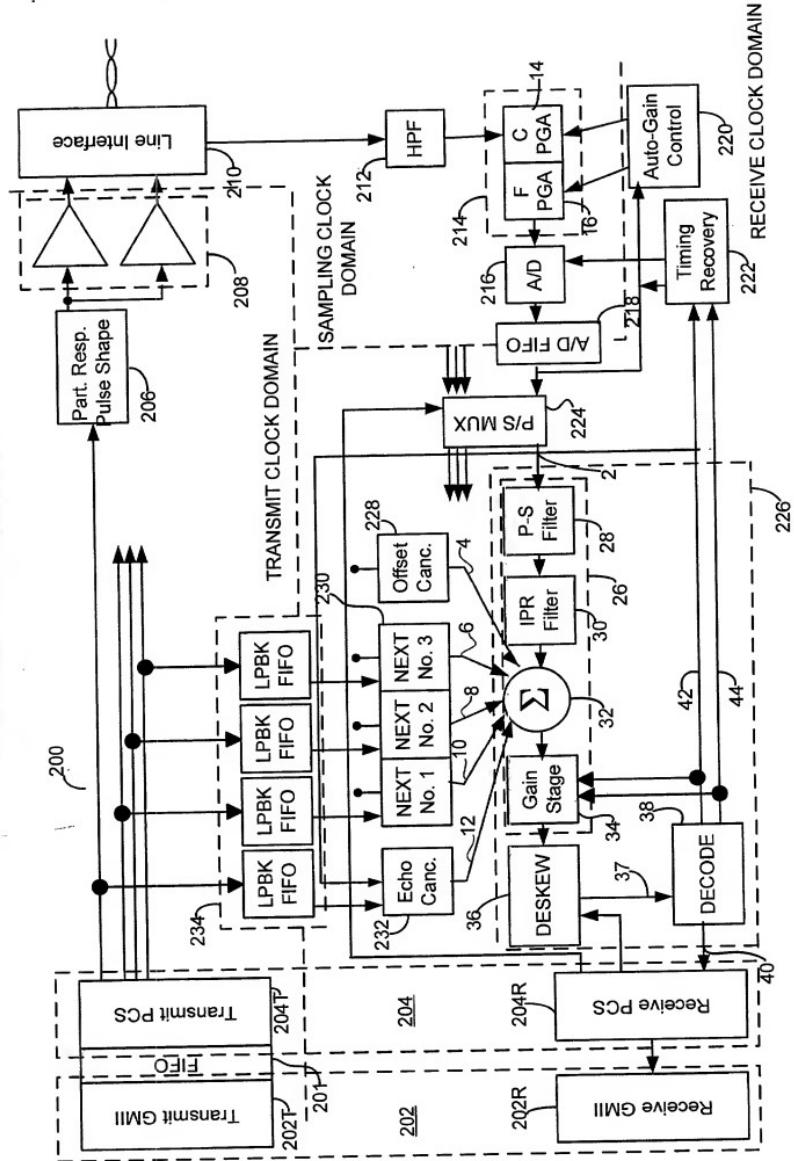


Figure 2

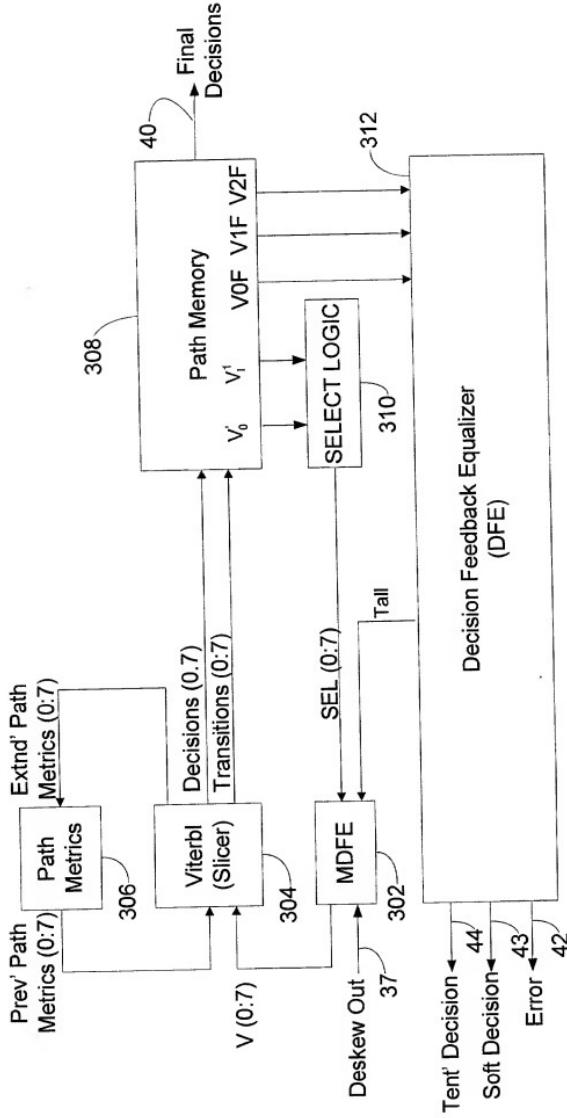


Figure 3

2010E0-5243800T

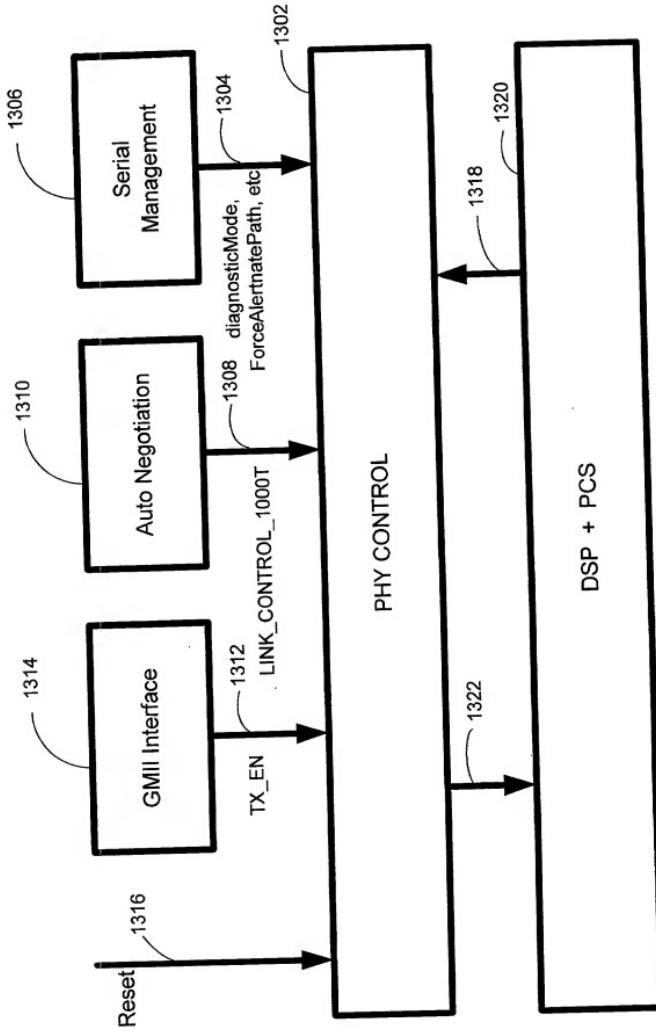


FIG. 4

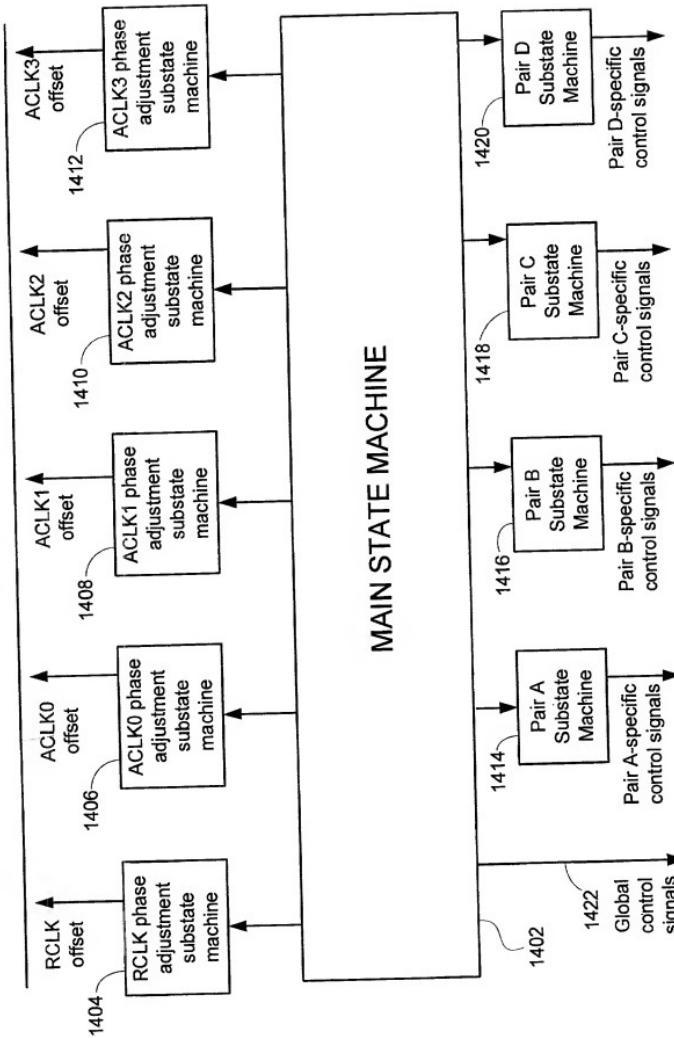


Figure 5

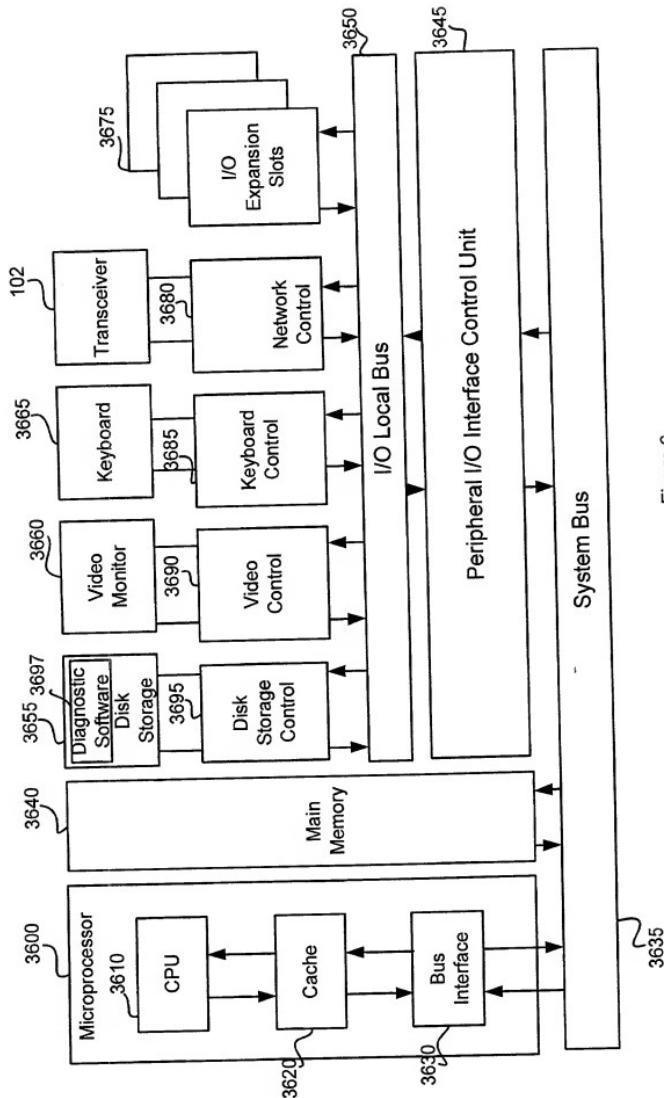


Figure 6

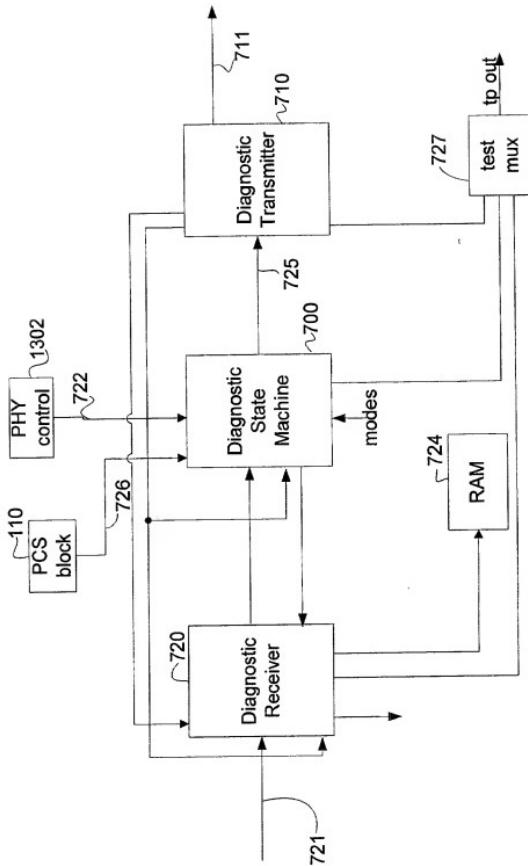


Figure 7

Master - no FEXT

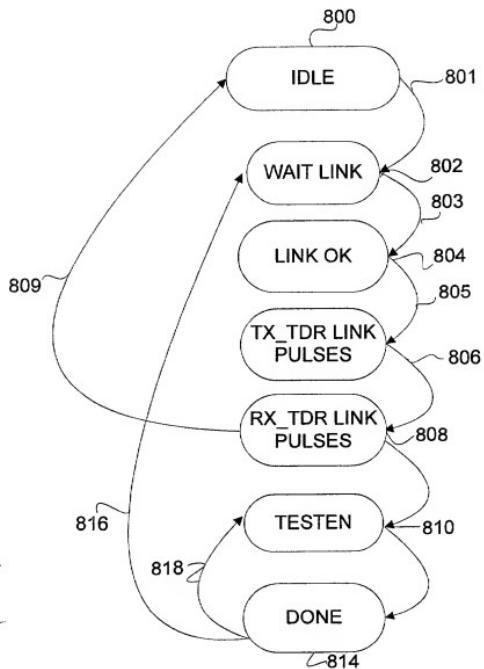


Figure 8

10086725 · 030402

slave - no FEXT

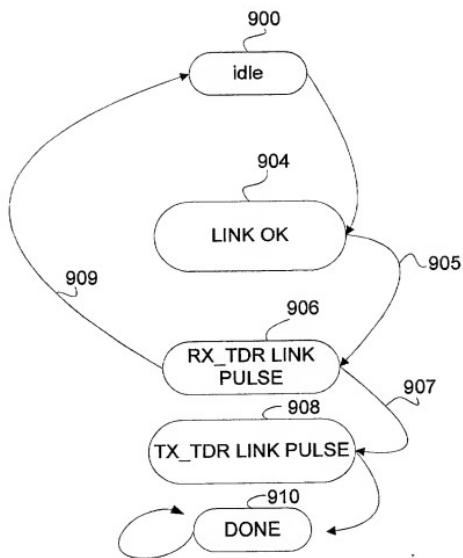


Figure 9

Echo/Next - no extphy

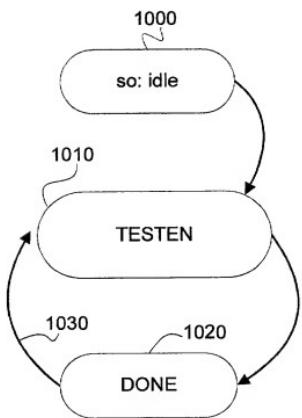


Figure 10

10086725 - 030402

Master - FEXT

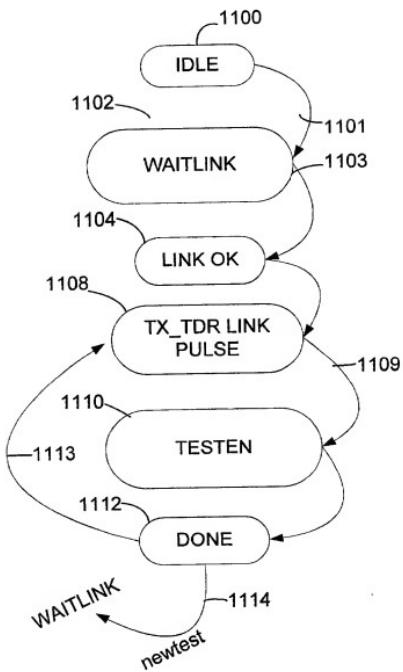


Figure 11

Slave- fext

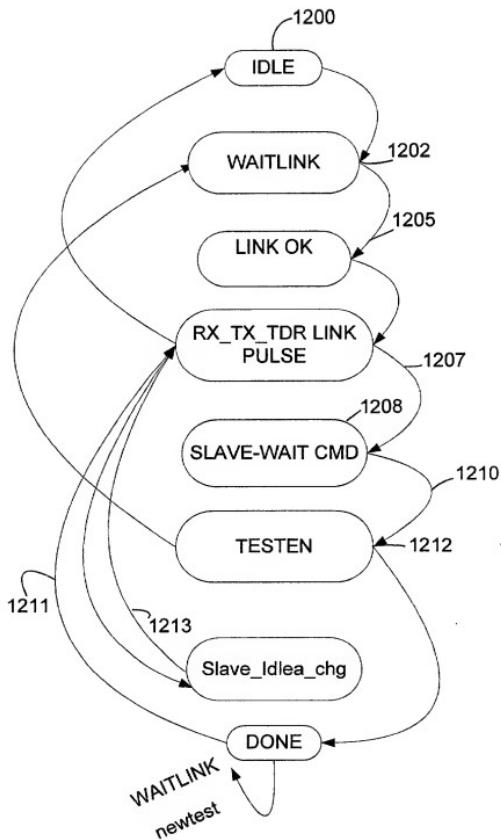


Figure 12

linkdet fsm

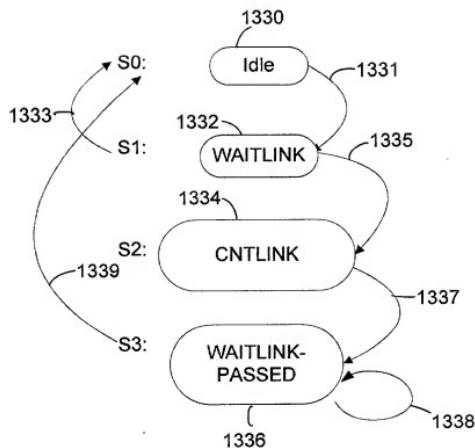
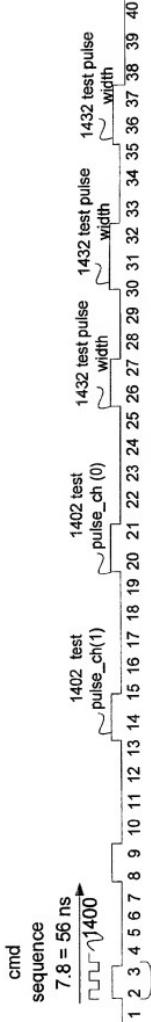


Figure 13

201000 * 522/9800T

FEXT: and signaling



max # times asserted = 3 T cycles

min # times asserted = 2 T cycles



Figure 14

2010-03-09 22:38:00T

LINKPULSE/TEST PULSE TIMING - NO FEXT

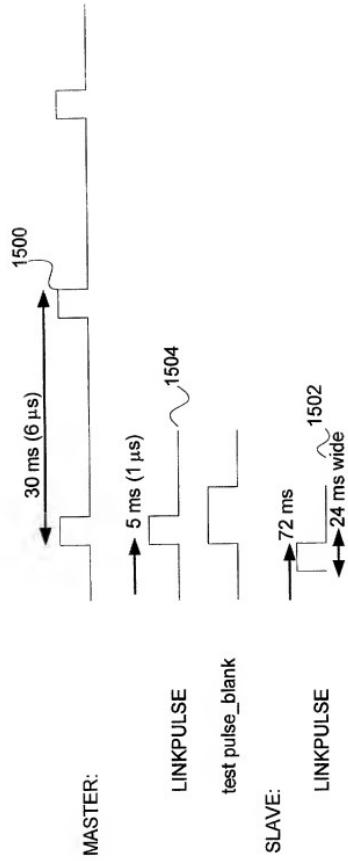


Figure 15

MDIX: Link pulse - no fext

1) No mdix

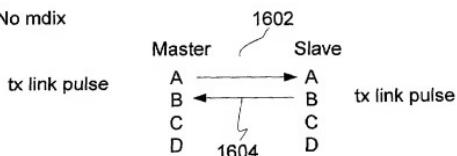


Figure 16A

2) mdix

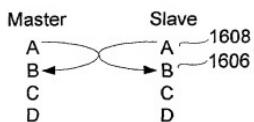


Figure 16B

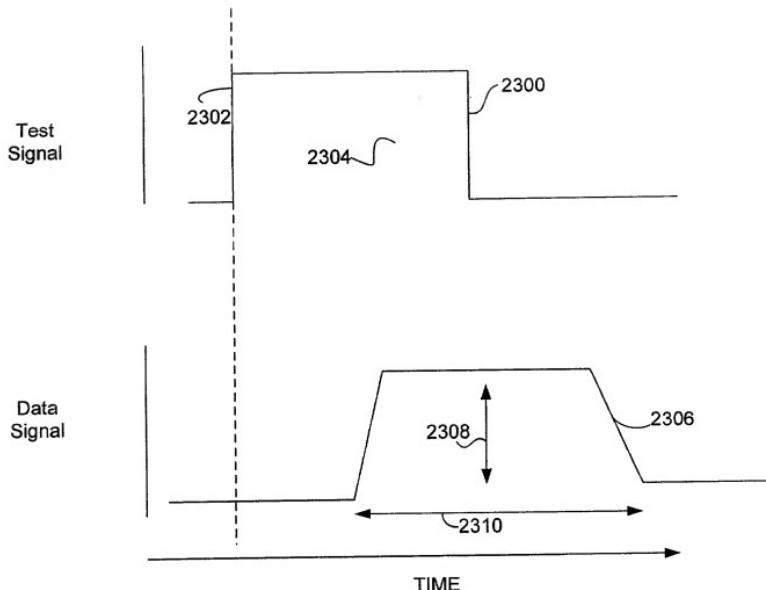


Figure 17

10086725, 030402

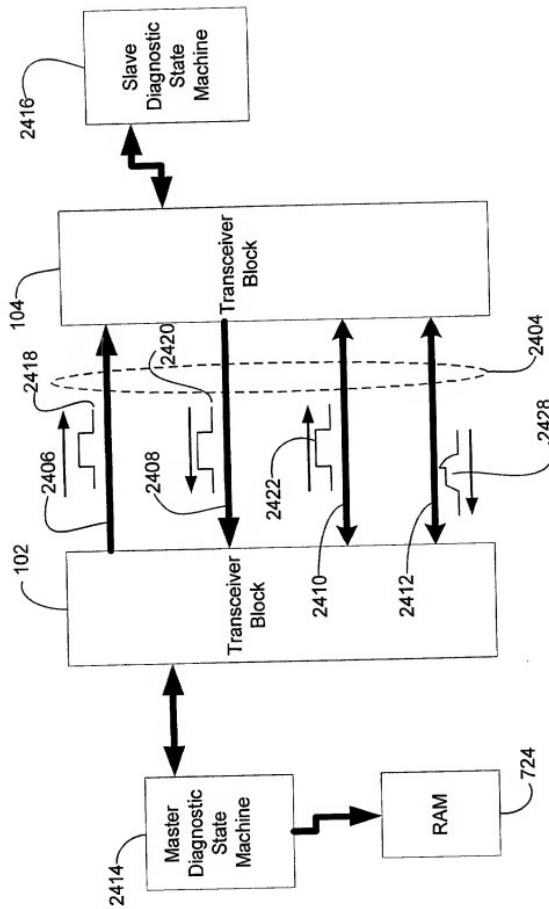


Figure 18

20100309 5249800Y

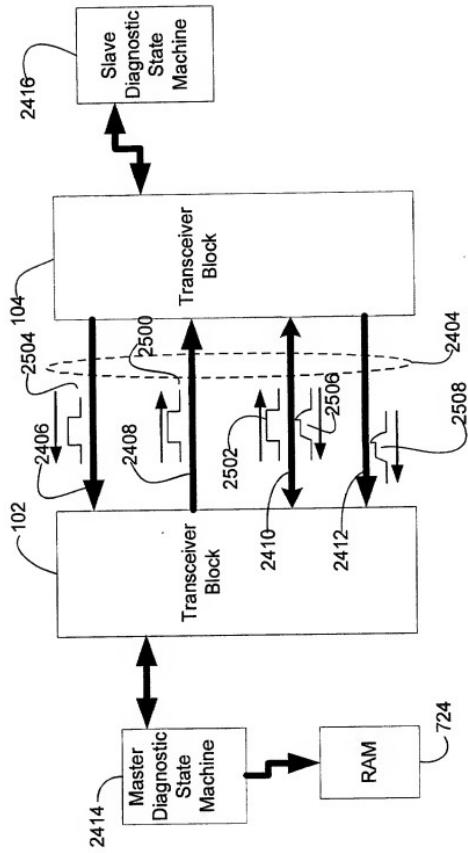


Figure 19

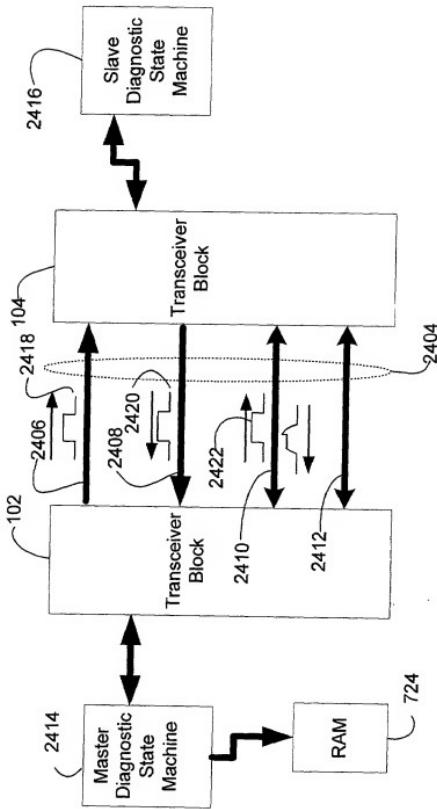


Figure 20